

Corrigendum-2

NIT No.: IACS-P/SCSSA/24-25/10

Dated: 21.06.2024

Previous venue of opening the price bid of the tender: Raychudhuri Hall

Revised venue of opening the price bid of the tender: TRC-504

MASTER NODE WITH STORAGE	
Features	Specification
Processor	X86 64 16C/32T 2.9G 24M 11.2GT 185W 4189 M1
No. of Processors	Minimum two or More Per Node. 32 Cores (64 threads) Per Node.
RAM	128 GB DDR4-3200 ECC RDIMM, Scalability after populating current set of DIMMS must be up to 3 TB or Higher
SSD	2 x 960GB, SATA, 2.5", 3D TLC, 5DWPD
HDD	6 x 12 TB NL SAS/SATA ENT HDD 7.2 K RPM
Raid Controller	Raid Controller Supports Raid 0,1,5,6,50,60 with 8Gb Cache
Chassis	2U form factor
P. Supply	1000W Redundant Power Supplies
Compute Nodes X 9 Nos	
Processor	X86 64 16C/32T 2.9G 24M 11.2GT 185W 4189 M1 or higher
No Of Processors	Minimum two or More Per Node. 32 Cores (64 threads) Per Node.
RAM	256 GB DDR4-3200 ECC RDIMM, Scalability after populating current set of DIMMS must be up to 3 TB or Higher
SSD	960GB, SATA, 2.5", 3D TLC, 5DWPD
Chassis	2 U form factor
P. Supply	1000W Redundant Power Supplies
Ethernet switch	1 (one) 24 port Gigabit ethernet switch with all necessary accessories and cables

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