

Corrigendum-II

NIT No. IACS-P/SCSDG/2024-25/20

Date:05/07/2024

1. Changes in specification in CPU with high memory nodes:

	Before	Corrected
CPU Node with High Memory	Processor(s): 2 x 6334 8C/16T 3.6G 18M 11.2GT 165W	Processor(s): 1 x 6334 8C/16T 3.6G 18M 11.2GT 165W

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